

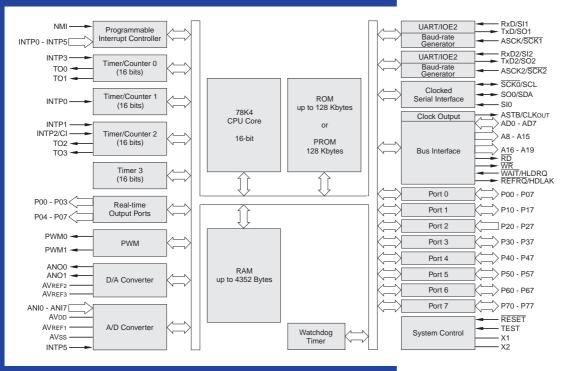


16-bit Microcontrollers

Description	The μ PD78403x microcontrollers are members of NEC's 16-bit 78K4 family. This family is pin-compatible to the predecessor 78402x and 7823x families. They also offer an easy migration path from NEC's 78K0 8-bit microcontrollers for applications requiring large on-chip memory and high processing performance.			
Applications	µPD78403x devices are designed for use in printers, telephones and DC motor control applications.			
Features	 ROMless version available Up to 128 Kbytes Mask ROM and PROM versions Up to 4352 bytes RAM 1 Mbyte linear address space 125 ns instruction cycle time at 32 MHz Bit manipulation over the entire address space 3 serial interfaces including 2 UARTs 8-channel A/D converter 2-channel D/A converter 	 4 x 16-bit timer/counter 2-channel PWM output with 12-bit resolution 64 I/O pins (46 for ROMless version) Interrupt controller (4 programmable priority levels) External memory interface with bus hold function Clock prescaler (enabled by software) Standby control (HALT, IDLE, STOP mode) Power supply voltage: 2.7 – 5.5 V 		

• 80-pin QFP and TQFP packages

Block Diagram



8-channel real-time output port



Functional Block Description

CPU	The 78K4 CPU features 8 general register banks with 8 x 16-bit or 16 x 8-bit registers plus 4 x 8-bit registers for 24-bit address expansion. The general-purpose registers are mapped to the internal RAM. Register banks can be switched by software or context switching. Registers can be manipulated in 8-bit units. Pairs of 8-bit general-purpose registers can be manipulated in 16-bit units. For 24-bit address expansion, four of the 16-bit registers can be combined with 8-bit registers. High speed instruction fetch is made possible by a prefetch queue with 5 bytes for internal fetch and 3 bytes for external fetch.
Memory	µPD78403x devices have a 1 Mbyte linear address space and offer an ample choice of on- chip memory combinations, including a ROMless version and a PROM version (see table).
Ports	ROM/PROM-based devices have 8 (8) input pins, 64 (48) input/output pins, 24 (8) of which are capable of directly driving LEDs. 54 (32) input/output pins have internal pull-up resistors that can be enabled via software. 8 port pins can drive Darlington transistors directly. Numbers in brackets apply to ROMless versions.
Real Time Output Ports	An interrupt generated by a timer/counter or an external interrupt causes these ports to output data which has previously been stored in a buffer for a jitter-free pulse output.
A/D Converter	An 8-channel A/D converter with 8-bit resolution is provided on chip using successive approximation. The overall power consumption of the system can be reduced by disabling the A/D resistor chain.
D/A Converter	A 2-channel D/A converter with 8-bit resolution uses the R-2R resistor ladder method. The D/A converter can be used in real-time mode. In this case, analog voltage output is synchronized with the output trigger. This mode allows sine wave generation.
Serial Interface	Three serial interfaces include two full-duplex UARTs with on-chip baud rate generators (conforming to RS232). The maximum UART speed is 2 Mbps. The additional CSI (Clocked serial Interface) supports data transfer of up to 1.44 Mbps and can be used in I ² C mode (at 400kHz) on the μ PD78403xY.
Timer	All devices have 4 channels of 16-bit timers controlled by 7 interrupts. All timers are equipped with a capture register and three timers can be used as event counters and feature additional compare registers. Two timers are able to output PWM/PPG or pulses. The on-chip watchdog timer monitors CPU operation.
PWM Output	Two channels of PWM output circuits with 12-bit resolution are provided. At this resolution PWM frequencies up to 62.5KHz can be generated. Both channels can select either a high or low active level. These outputs are ideal for controlling the speed of DC motors.
Clock Generator	The on-chip clock generator oscillates at frequencies between 2 and 32 MHz.
Interrupt Controller	Powerful interrupt handling capability is based on a macro service, context switching and vectored interrupts. An external non-maskable interrupt is provided. The interrupt controller handles the different maskable and non-maskable interrupt requests issued by internal peripheral hardware (17 sources plus 1 for I ² C version) or external devices (6 sources plus 1 for I ² C version).



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Ordering Information

Devices

Order Number	ROM (Kbytes)	PROM (Kbytes)	RAM (bytes)
µPD784031	None	—	2048
µPD784035	48	—	2048
µPD784036	64	—	2048
µPD784037	96	—	3584
µPD784038	128	—	4352
µPD78P4038	—	128	4352

Device orders must specify the package code GC (QFP 14 x 14 mm), GK (TQFP 12 x 12 mm) or KK-T. (LCC, for OTP only). All devices are also available with I²C bus. The part number of I²C devices is obtained by adding a 'Y' before the package code.

Documentation	Doc Number	Devices	Туре
	U11933EE3V0CD00	NEC Microcontrollers	CD-ROM
	U11316EJ3V0UM00	μPD784031, μPD784035/6/7	User's Manual Hardware
	U10741EJ1V0PM00	µPD784035Y/6Y/7Y	PPI
	U10905EJ6V0UM00	78K4 Series (instructions)	User's Manual Software
	U16847EJ2V0DS00	µPD784035/6/7	Data Sheet
	U11507EJ1V0DS00	µPD784031	Data Sheet
	U11504EJ1V0DS00	µPD784031Y	Data Sheet
	U10742EJ1V0PM00	µPD78P4038Y	Data Sheet
	U10848EJ1V0DS00	µPD78P4038	Data Sheet
	U13285EJ1V0AN00	µPD78403x	Application note (HW basics)

Tools

Order Number	Description	Туре
CCMSD-I3HD-784xx	C Compiler/Assembler	Software
DSWIN-I3HD-784xx	Simulator	Software
DIWIN-I3HD-784xx	GUI Debugger	Software
IE-78400-R, IE-78400-R-EM,	In-circuit Emulator	Hardware
IE-70000-PC-IF-C		
IE-784038-R-EM1	Emulation Board	Hardware
EP-78230GC-R	Emulation Probe	Hardware
EP-78054GK-R	Emulation Probe	Hardware
EV9200GC-80	LCC Socket	Hardware
TGK-080SDW	LCC Socket	Hardware
PA-78P4026GC	Programming Adapter	Hardware
PA-78P4026GK	Programming Adapter	Hardware
PA-78P4026KK-T	Programming Adapter	Hardware
FLASHMASTER	Flash Programmer	Hardware

For further information on NEC's 78K4 family or other NEC products visit our European website at **www.nec.de**



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